



YEA SHIN TECHNOLOGY CO., LTD

YS2305

P-Channel Enhancement MOSFET

VDS= -20V, ID= -4.2A

(Pb) (H)

DESCRIPTION

The YS2305 is the highest performance trench P-Ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the small power switching and load switch applications.

The YS2305 meet the RoHS and Green Product requirement with full function reliability approved.

FEATURES

- Advanced High Cell Density Trench Technology
- Super Low Gate Charge

MARKING

2305

PACKAGE INFORMATION

| Package | MPQ | Leader Size |
|---------|-----|-------------|
| SC-59 | 3K | 7 inch |

ORDER INFORMATION

| Part Number | Type |
|-------------|---------------------------------|
| YS2305 | Lead (Pb)-free and Halogen-free |

ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Ratings | | Unit |
|---|--------------------------|---------------------|--------------|------|
| | | $\leq 10\text{sec}$ | Steady State | |
| Drain-Source Voltage | V_{DS} | | -20 | V |
| Gate-Source Voltage | V_{GS} | | ± 12 | V |
| Drain Current ¹ , @ $V_{GS} = -10\text{V}$ | I_D | -4.2 | -3.7 | A |
| | | -3.5 | -3 | |
| Pulsed Drain Current ³ | I_{DM} | | -30 | A |
| Power Dissipation | $T_A = 25^\circ\text{C}$ | P_D | 1.4 | W |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | | -55~150 | °C |

Thermal Resistance Data

| | | | |
|--|-----------------|-------------------------|------|
| Thermal Resistance Junction-Ambient ¹ | $R_{\theta JA}$ | $\leq 10\text{sec}, 90$ | °C/W |
| | | Steady State, 125 | |
| Thermal Resistance Junction-Ambient ² | | 270 | |
| Thermal Resistance Junction-Case ¹ | $R_{\theta JC}$ | 80 | |

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ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Test Conditions |
|---|-----------------------------------|-------------------------|------|-----------|------------------|---|
| Drain-Source Breakdown Voltage | BV_{DSS} | -20 | - | - | V | $\text{V}_{\text{GS}}=0, \text{I}_D = -250\mu\text{A}$ |
| Gate Threshold Voltage | $\text{V}_{\text{GS}(\text{th})}$ | -0.5 | - | -1.2 | V | $\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D = -250\mu\text{A}$ |
| Forward Transconductance | g_{fs} | - | 5.6 | - | S | $\text{V}_{\text{DS}}= -5\text{V}, \text{I}_D = -3\text{A}$ |
| Gate-Source Leakage Current | I_{GSS} | - | - | ± 100 | nA | $\text{V}_{\text{GS}}= \pm 12\text{V}$ |
| Drain-Source Leakage Current | $\text{T}_J=25^\circ\text{C}$ | I_{DSS} | - | -1 | μA | $\text{V}_{\text{DS}}= -20\text{V}, \text{V}_{\text{GS}}=0$ |
| | $\text{T}_J=55^\circ\text{C}$ | | - | -5 | | $\text{V}_{\text{DS}}= -20\text{V}, \text{V}_{\text{GS}}=0$ |
| Drain-Source On-Resistance ⁴ | $\text{R}_{\text{DS}(\text{ON})}$ | - | - | 53 | $\text{m}\Omega$ | $\text{V}_{\text{GS}}= -10\text{V}, \text{I}_D = -4.5\text{A}$ |
| | | - | - | 65 | | $\text{V}_{\text{GS}}= -4.5\text{V}, \text{I}_D = -4.2\text{A}$ |
| | | - | - | 100 | | $\text{V}_{\text{GS}}= -2.5\text{V}, \text{I}_D = -2\text{A}$ |
| | | - | - | 250 | | $\text{V}_{\text{GS}}= -1.8\text{V}, \text{I}_D = -1\text{A}$ |
| Total Gate Charge | Q_g | - | 11.9 | - | nC | $\text{I}_D = -3\text{A}$ |
| Gate-Source Charge | Q_{gs} | - | 1.8 | - | | $\text{V}_{\text{DS}}= -15\text{V}$ |
| Gate-Drain ("Miller") Charge | Q_{gd} | - | 3 | - | | $\text{V}_{\text{GS}}= -4.5\text{V}$ |
| Turn-on Delay Time | $\text{T}_{\text{d}(\text{on})}$ | - | 6.6 | - | nS | $\text{V}_{\text{DD}}= -15\text{V}$ |
| Rise Time | T_r | - | 27.8 | - | | $\text{V}_{\text{GS}}= -4.5\text{V}$ |
| Turn-off Delay Time | $\text{T}_{\text{d}(\text{off})}$ | - | 46.2 | - | | $\text{I}_D = -3\text{A}$ |
| Fall Time | T_f | - | 20.6 | - | | $\text{R}_G=3.3\Omega$ |
| Input Capacitance | C_{iss} | - | 920 | - | pF | $\text{R}_L=5\Omega$ |
| Output Capacitance | C_{oss} | - | 73 | - | | $\text{V}_{\text{GS}}=0$ |
| Reverse Transfer Capacitance | C_{rss} | - | 71 | - | | $\text{V}_{\text{DS}}= -15\text{V}$ |
| Source-Drain Diode | | | | | | |
| Forward on Voltage ⁴ | V_{SD} | - | - | -1.2 | V | $\text{I}_S = -1.2\text{A}, \text{V}_{\text{GS}}=0$ |
| Continuous Source Current ¹ | I_S | - | - | -3.7 | A | |
| Pulsed Source Current ³ | I_{SM} | - | - | -15 | | |

Notes:

1. Surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. When mounted on Min. copper pad.
3. Pulse width limited by maximum junction temperature.
4. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

DEVICE CHARACTERISTICS

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CHARACTERISTIC CURVE

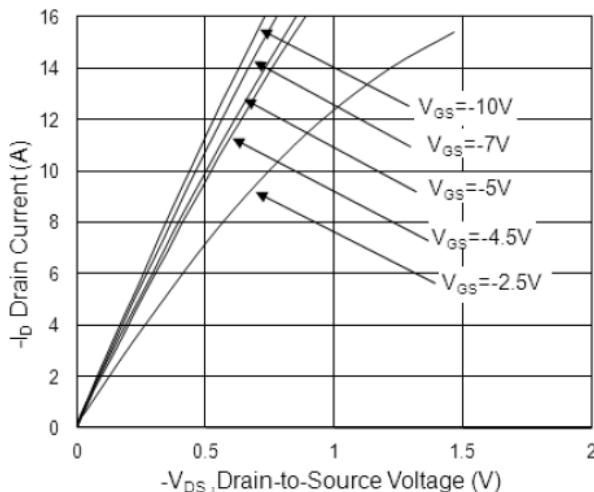


Fig.1 Typical Output Characteristics

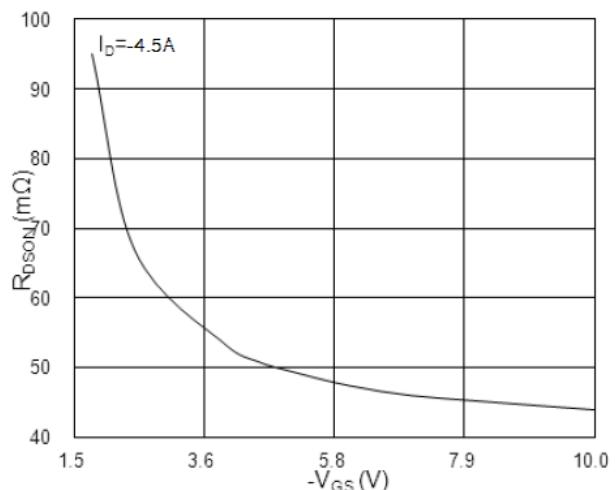


Fig.2 On-Resistance vs. G-S Voltage

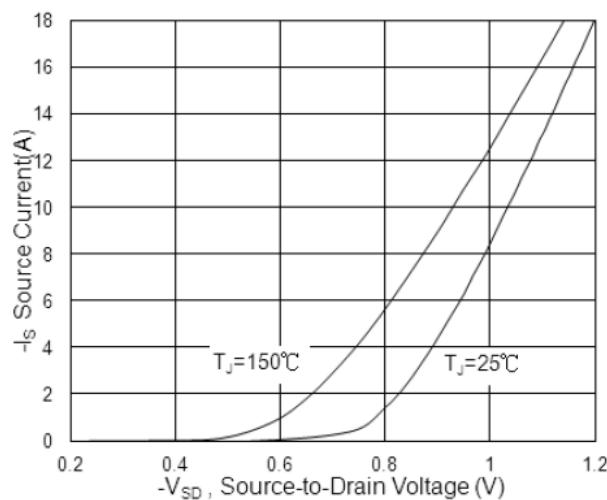


Fig.3 Forward Characteristics Of Reverse

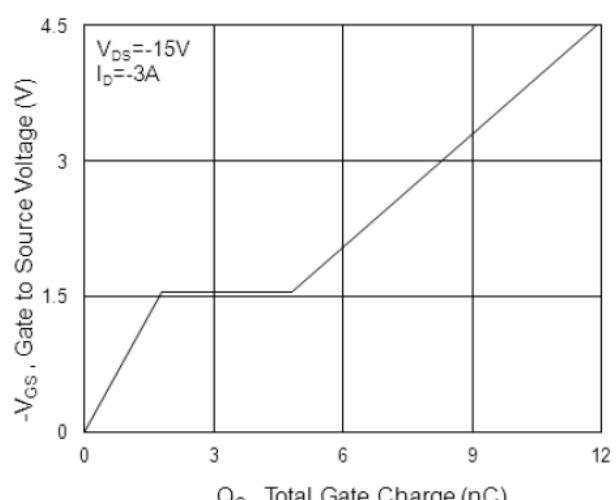


Fig.4 Gate-Charge Characteristics

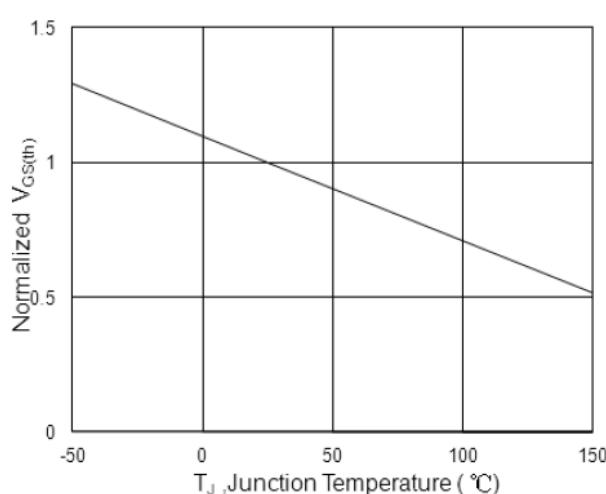


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

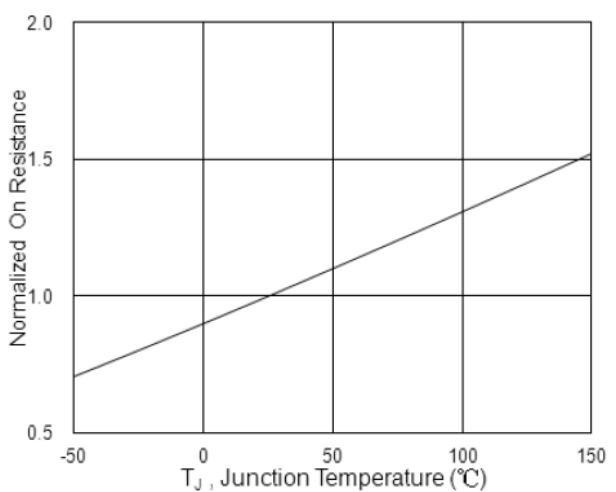


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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CHARACTERISTIC CURVE

