



YEA SHIN TECHNOLOGY CO., LTD

YS2305A

## P- Channel Enhancement MOSFET

VDS= -30V, ID= -4.2A



### DESCRIPTION

The YS2305A is the highest performance trench P-Ch MOSFETs with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the small power switching and load switch applications.

The YS2305A meet the RoHS and Green Product requirement with full function reliability approved.

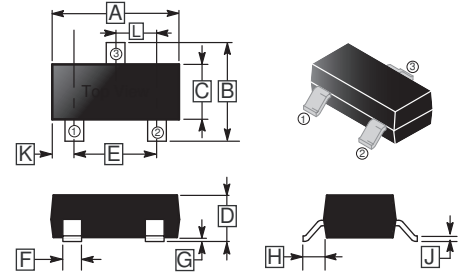
### FEATURES

- Advanced High Cell Density Trench Technology
- Super Low Gate Charge

### MARKING

2305A

### SC-59



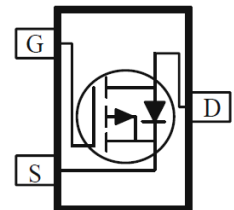
REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	0.10 REF.	
B	2.10	3.00	H	0.40 REF.	
C	1.20	1.70	J	0.047   0.207	
D	0.89	1.40	K	0.5 REF.	
E	2.00 Typ.		L	0.95 REF.	
F	0.30	0.50			

### PACKAGE INFORMATION

Package	MPQ	Leader Size
SC-59	3K	7 inch

### ORDER INFORMATION

Part Number	Type
YS2305A	Lead (Pb)-free and Halogen-free



### ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Ratings		Unit
			≤ 10sec	Steady State	
Drain-Source Voltage		V <sub>DS</sub>	-30		V
Gate-Source Voltage		V <sub>GS</sub>	±12		V
Drain Current <sup>1</sup> , @VGS= -10V	T <sub>A</sub> =25℃	I <sub>D</sub>	-4.2	-3.7	A
	T <sub>A</sub> =70℃		-3.5	-3	
Pulsed Drain Current <sup>3</sup>		I <sub>DM</sub>	-30		A
Power Dissipation	T <sub>A</sub> =25℃	P <sub>D</sub>	1.4		W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55~150		℃
Thermal Resistance Data					
Thermal Resistance Junction-Ambient <sup>1</sup>		R <sub>θJA</sub>	≤ 10sec, 90		℃/W
			Steady State, 125		
Thermal Resistance Junction-Ambient <sup>2</sup>			270		
Thermal Resistance Junction-Case <sup>1</sup>		R <sub>θJC</sub>	80		

# YS2305A

## ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C unless otherwise specified)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	-30	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> = -250μA
Gate Threshold Voltage		V <sub>GS(th)</sub>	-0.5	-	-1.2	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = -250μA
Forward Transconductance		g <sub>fs</sub>	-	5.6	-	S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -3A
Gate-Source Leakage Current		I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> = ±12V
Drain-Source Leakage Current	T <sub>J</sub> =25°C	I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> = -24V, V <sub>GS</sub> =0
	T <sub>J</sub> =55°C		-	-	-5		V <sub>DS</sub> = -24V, V <sub>GS</sub> =0
Drain-Source On-Resistance <sup>4</sup>		R <sub>DS(ON)</sub>	-	-	60	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -3.2A
			-	-	80		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3A
			-	-	150		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -2A
			-	-	250		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -1A
Total Gate Charge		Q <sub>g</sub>	-	11.9	-	nC	I <sub>D</sub> = -3A
Gate-Source Charge		Q <sub>gs</sub>	-	1.8	-		V <sub>DS</sub> = -15V
Gate-Drain (“Miller”) Charge		Q <sub>gd</sub>	-	3	-		V <sub>GS</sub> = -4.5V
Turn-on Delay Time		T <sub>d(on)</sub>	-	6.6	-	nS	V <sub>DD</sub> = -15V
Rise Time		T <sub>r</sub>	-	27.8	-		V <sub>GS</sub> = -4.5V
Turn-off Delay Time		T <sub>d(off)</sub>	-	46.2	-		I <sub>D</sub> = -3A
Fall Time		T <sub>f</sub>	-	20.6	-		R <sub>G</sub> =3.3Ω R <sub>L</sub> =5Ω
Input Capacitance		C <sub>iss</sub>	-	920	-	pF	V <sub>GS</sub> =0
Output Capacitance		C <sub>oss</sub>	-	73	-		V <sub>DS</sub> = -15V
Reverse Transfer Capacitance		C <sub>rss</sub>	-	71	-		f=1MHz
Source-Drain Diode							
Forward on Voltage <sup>4</sup>		V <sub>SD</sub>	-	-	-1.2	V	I <sub>S</sub> = -1.2A, V <sub>GS</sub> =0
Continuous Source Current <sup>1</sup>		I <sub>S</sub>	-	-	-3.7	A	
Pulsed Source Current <sup>3</sup>		I <sub>SM</sub>	-	-	-15		

Notes:

1. Surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
2. When mounted on Min. copper pad.
3. Pulse width limited by maximum junction temperature.
4. The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%.

# DEVICE CHARACTERISTICS

## YS2305A

### CHARACTERISTIC CURVE

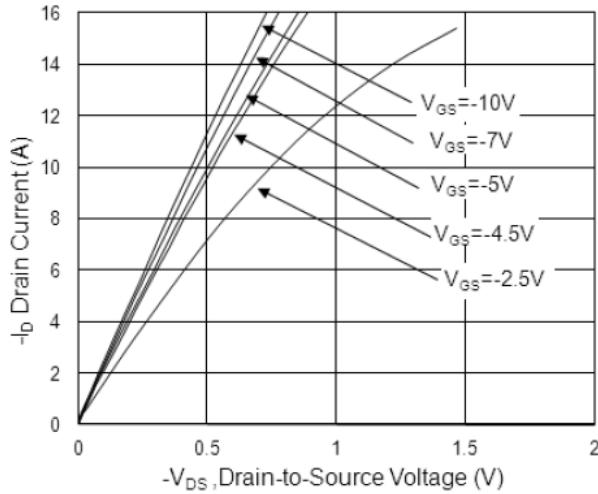


Fig.1 Typical Output Characteristics

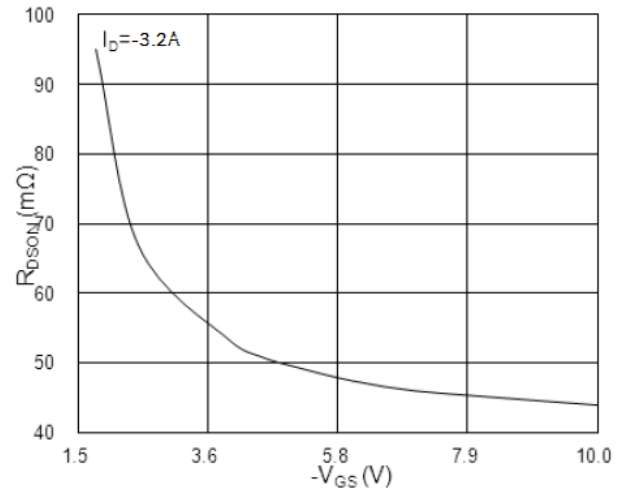


Fig.2 On-Resistance vs. G-S Voltage

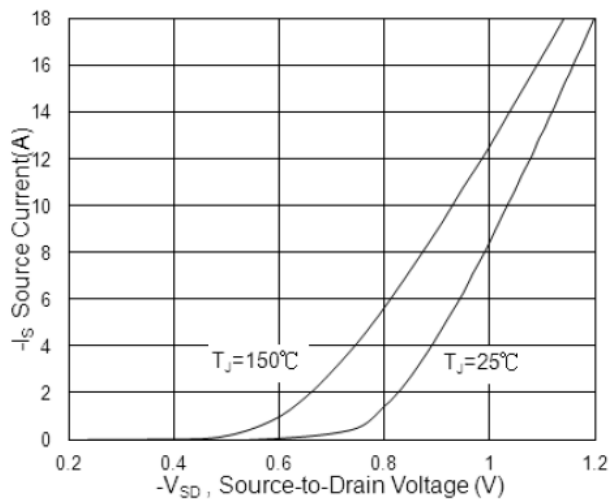


Fig.3 Forward Characteristics Of Reverse

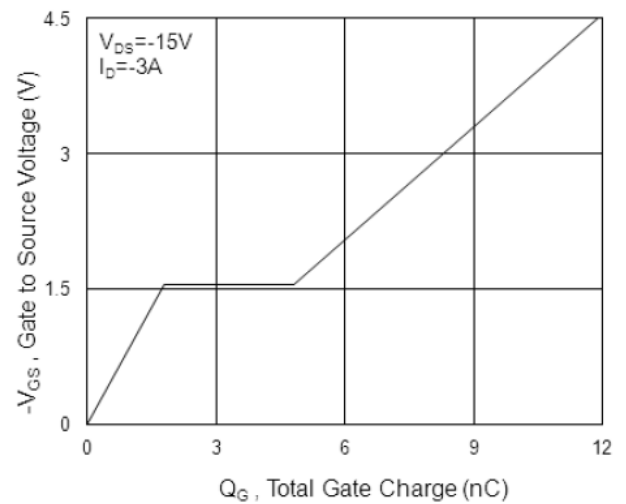


Fig.4 Gate-Charge Characteristics

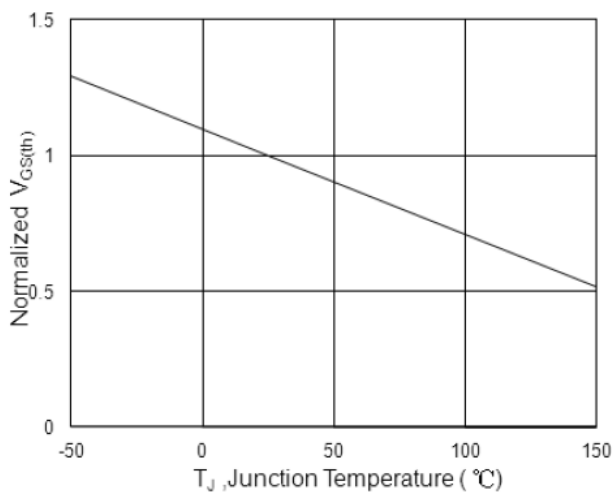


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

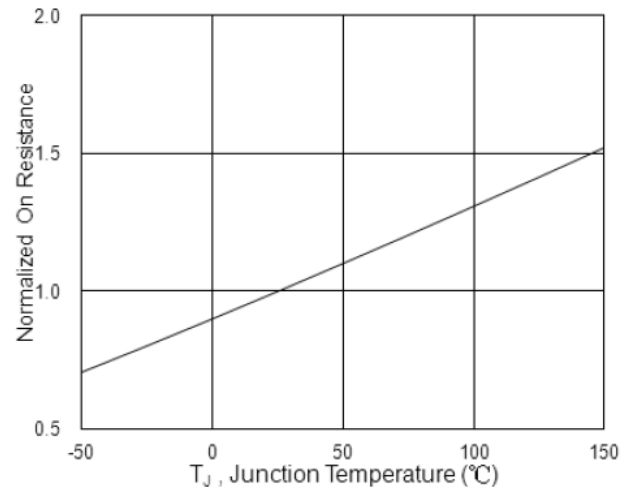


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$

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## CHARACTERISTIC CURVE

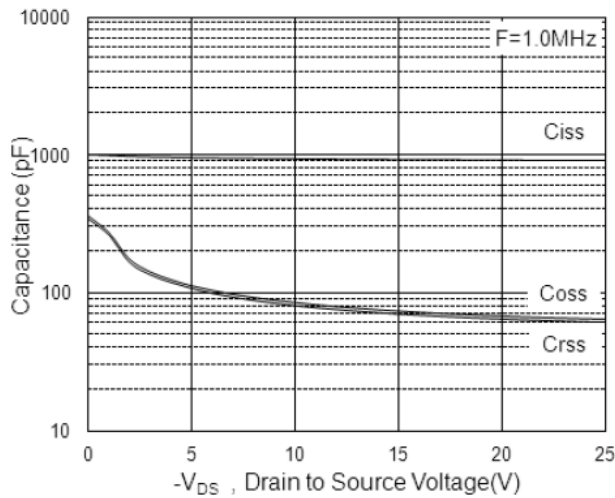


Fig.7 Capacitance

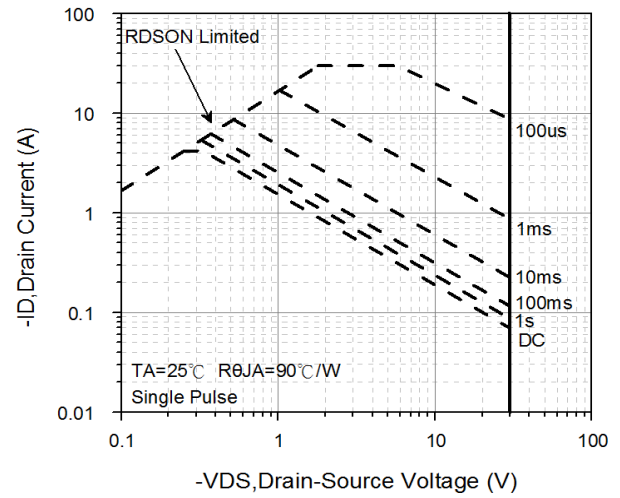


Fig.8 Safe Operating Area

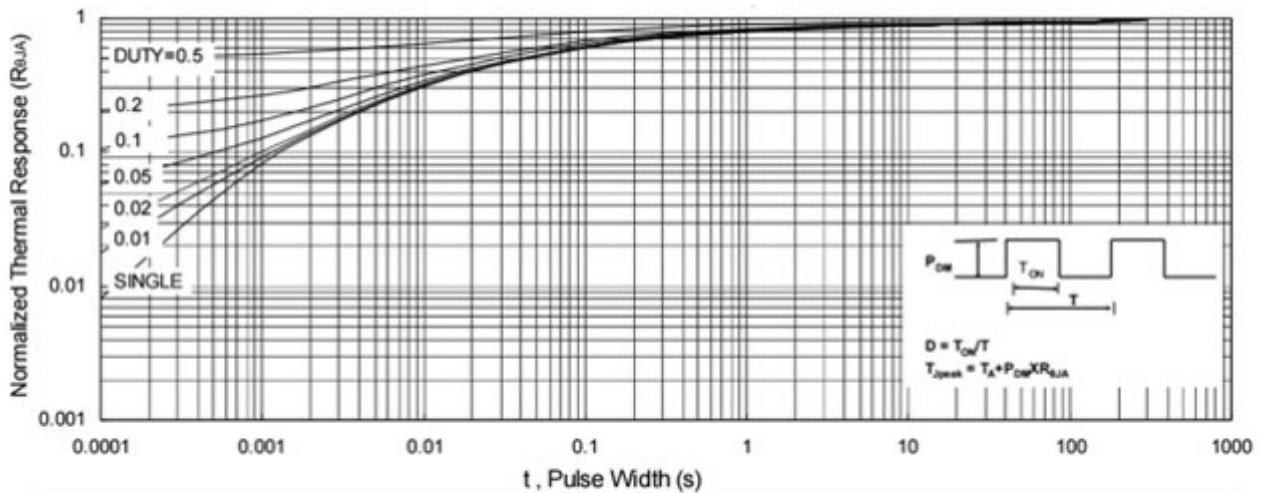


Fig.9 Normalized Maximum Transient Thermal Impedance

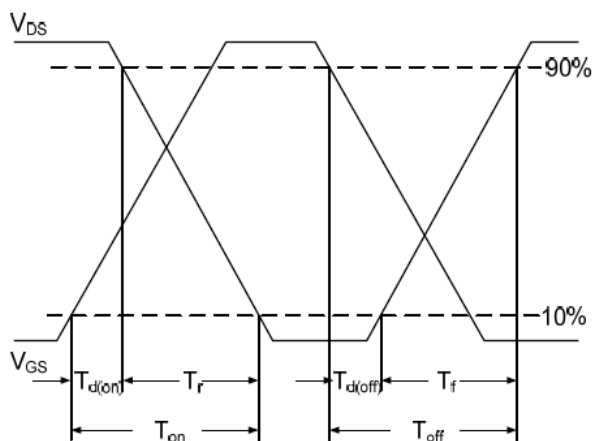


Fig.10 Switching Time Waveform

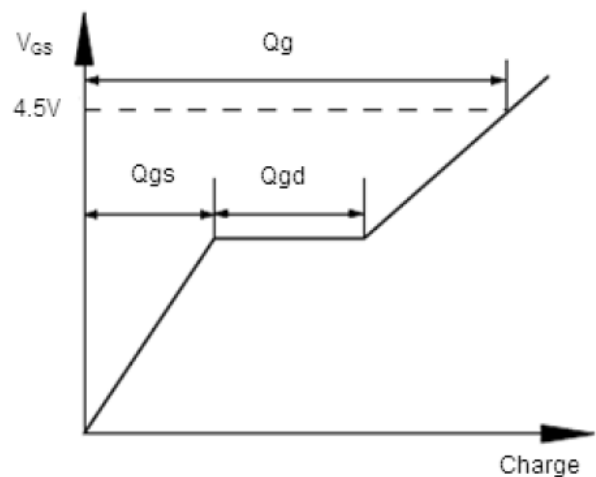


Fig.11 Gate Charge Waveform