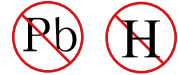




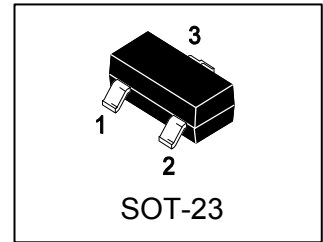
# N-Channel Enhancement MOSFET

VDS= 60V, ID= 115mA



## 1. FEATURES

- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- ESD Protected:1000V



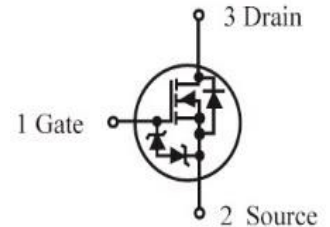
## 2. DEVICE MARKING AND ORDERING INFORMATION

### MARKING

702

### PACKAGE INFORMATION

Package	Shipping
SOT-23	3000/Tape&Reel



## 3. MAXIMUM RATINGS(Ta = 25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	VDSS	60	Vdc
Drain-Gate Voltage (RGS = 1.0 MΩ)	VDGR	60	Vdc
Drain Current	ID		mAdc
- Continuous TC = 25°C		±115	
TC = 100°C		±75	
- Pulsed (Note 1)	IDM	±800	
Gate-Source Voltage			
- Continuous	VGS	±20	Vdc
- Non-repetitive (tp ≤ 50µs)	VGSM	±40	Vdc

## 4. THERMAL CHARACTERISTICS

Parameter	Symbol	Limits	Unit
Total Device Dissipation, FR-5 Board (Note 2) @ TA = 25°C	PD	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient(Note 2)	RθJA	556	°C/W
Junction and Storage temperature	TJ, Tstg	-55~+150	°C

1. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.
2. FR-5 = 1.0×0.75×0.062 in.

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## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Drain–Source Breakdown Voltage ( $V_{GS} = 0, I_D = 10 \mu\text{Adc}$ )	$V_{(BR)DSS}$	60	–	–	Vdc
Zero Gate Voltage Drain Current ( $V_{GS} = 0, V_{DS} = 60 \text{ Vdc}$ )	$I_{DSS}$	– –	– –	1.0 500	$\mu\text{Adc}$
Gate–Body Leakage Current, Forward ( $V_{GS} = 20 \text{ Vdc}$ )	$I_{GSSF}$	–	–	1.0	$\mu\text{Adc}$
Gate–Body Leakage Current, Reverse ( $V_{GS} = -20 \text{ Vdc}$ )	$I_{GSSR}$	–	–	-1.0	$\mu\text{Adc}$

## ON CHARACTERISTICS (Note 3.)

Gate Threshold Voltage ( $V_{DS} = V_{GS}, I_D = 250 \mu\text{Adc}$ )	$V_{GS(th)}$	1.0	1.6	2.0	Vdc
On–State Drain Current ( $V_{DS} \geq 2.0 V_{DS(on)}, V_{GS} = 10 \text{ Vdc}$ )	$I_{D(on)}$	500	–	–	mA
Static Drain–Source On–State Voltage ( $V_{GS} = 10 \text{ Vdc}, I_D = 500 \text{ mAdc}$ ) ( $V_{GS} = 5.0 \text{ Vdc}, I_D = 50 \text{ mAdc}$ )	$V_{DS(on)}$	– –	– –	3.75 0.375	Vdc
Static Drain–Source On–State Resistance ( $V_{GS} = 10 \text{ V}, I_D = 500 \text{ mAdc}$ ) $T_C = 25^\circ\text{C}$ $T_C = 125^\circ\text{C}$ ( $V_{GS} = 5.0 \text{ Vdc}, I_D = 50 \text{ mAdc}$ ) $T_C = 25^\circ\text{C}$ $T_C = 125^\circ\text{C}$	$r_{DS(on)}$	– – – –	1.4 – 1.8 –	7.5 13.5 7.5 13.5	Ohms
Forward Transconductance ( $V_{DS} \geq 2.0 V_{DS(on)}, I_D = 200 \text{ mAdc}$ )	$g_{FS}$	80	–	–	mmhos

## DYNAMIC CHARACTERISTICS

Input Capacitance ( $V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz}$ )	$C_{iss}$	–	17	50	pF
Output Capacitance ( $V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz}$ )	$C_{oss}$	–	10	25	pF
Reverse Transfer Capacitance ( $V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz}$ )	$C_{rss}$	–	2.5	5.0	pF

## SWITCHING CHARACTERISTICS (Note 2.)

Turn–On Delay Time	$(V_{DD} = 25 \text{ Vdc}, I_D \cong 500 \text{ mAdc}, R_G = 25 \Omega, R_L = 50 \Omega, V_{gen} = 10 \text{ V})$	$t_{d(on)}$	–	7	20	ns
Turn–Off Delay Time		$t_{d(off)}$	–	11	40	ns

## BODY–DRAIN DIODE RATINGS

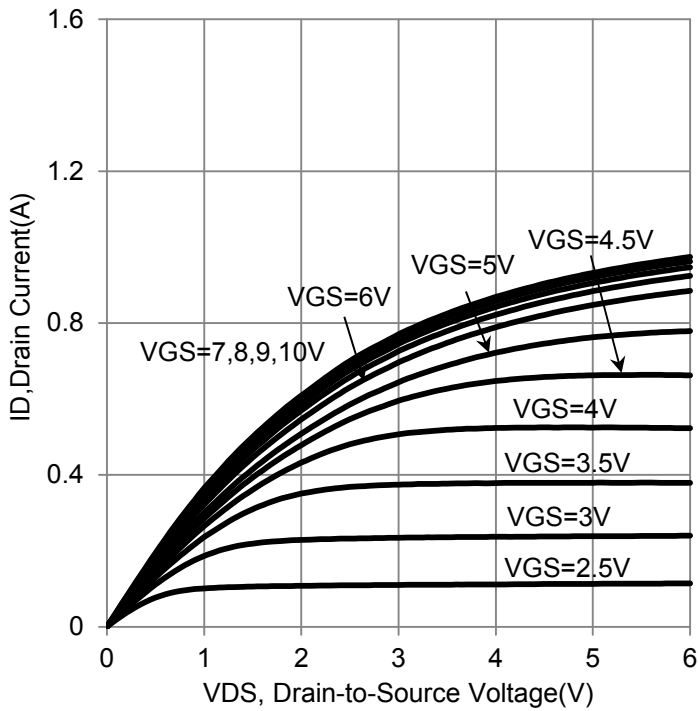
Diode Forward On–Voltage ( $I_S = 115 \text{ mAdc}, V_{GS} = 0 \text{ V}$ )	$V_{SD}$	–	–	-1.5	Vdc
Source Current Continuous (Body Diode)	$I_S$	–	–	-115	mAdc
Source Current Pulsed	$I_{SM}$	–	–	-800	mAdc

3. Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

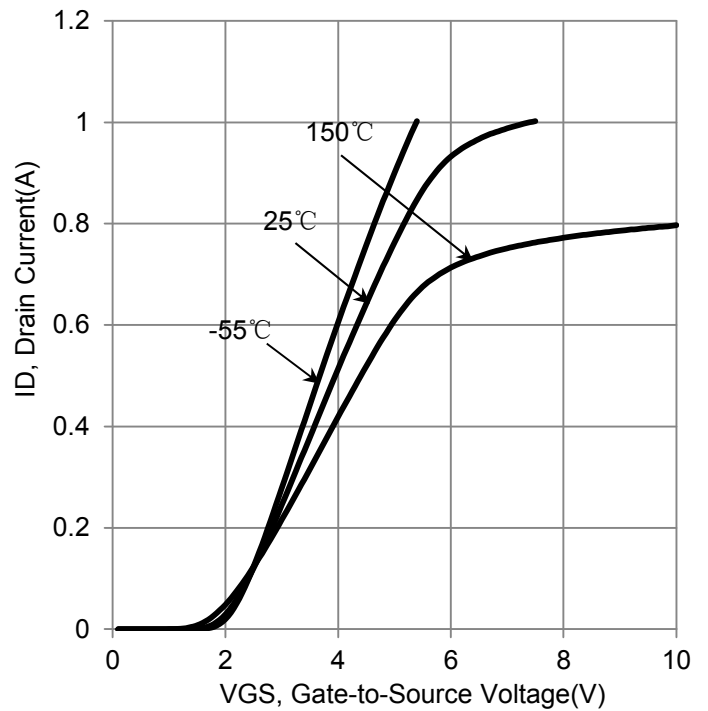
# DEVICE CHARACTERISTICS

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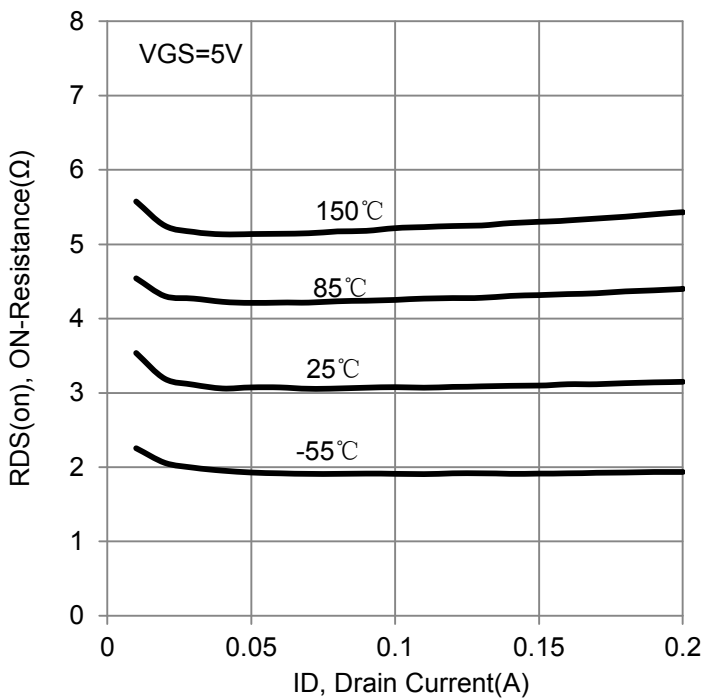
## 6. ELECTRICAL CHARACTERISTICS CURVES



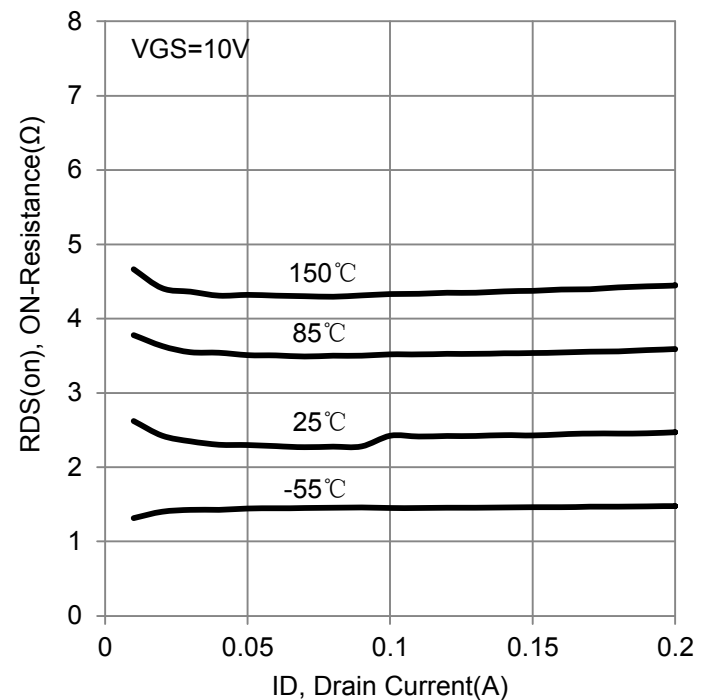
ON-Region Characteristics



Transfer Characteristics



RDS(on) vs. ID

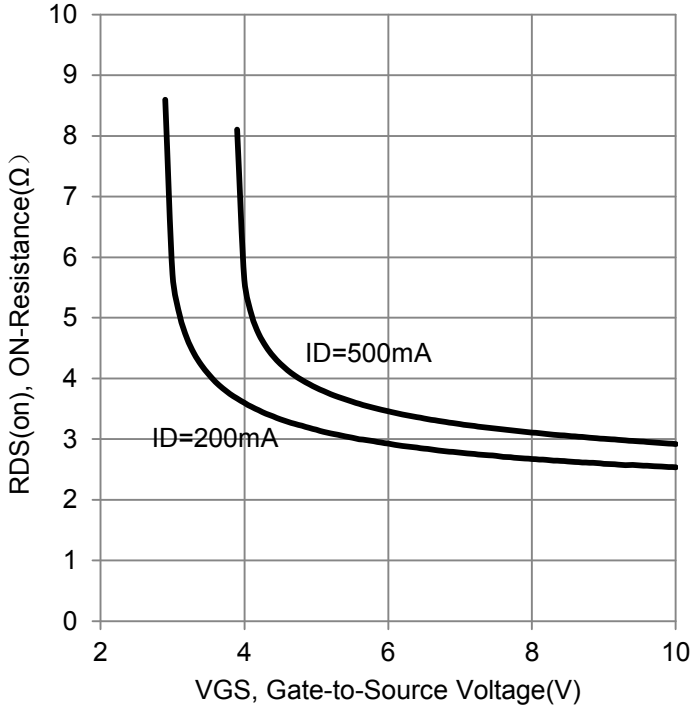


RDS(on) vs. ID

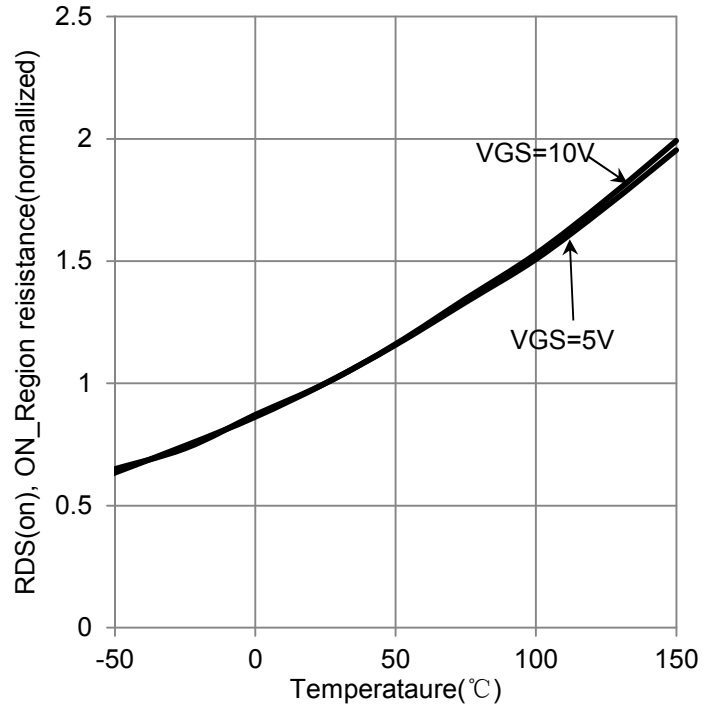
# DEVICE CHARACTERISTICS

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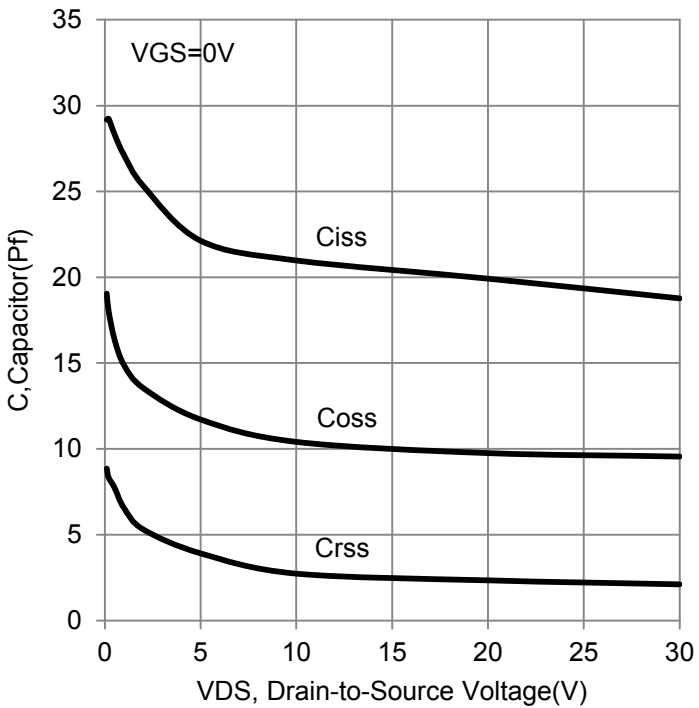
## 6. ELECTRICAL CHARACTERISTICS CURVES (Con.)



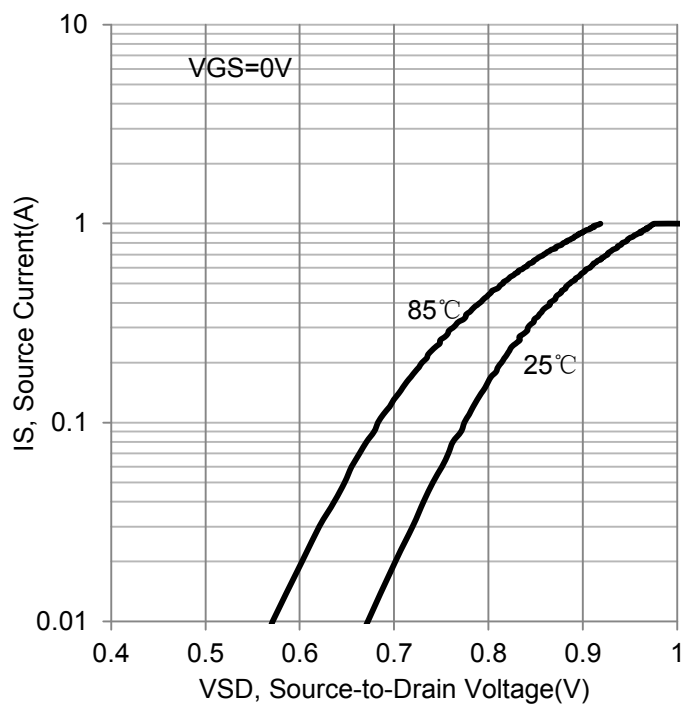
RDS(on) vs. VGS



RDS(on) vs. Temperature



Capacitor vs. VDS

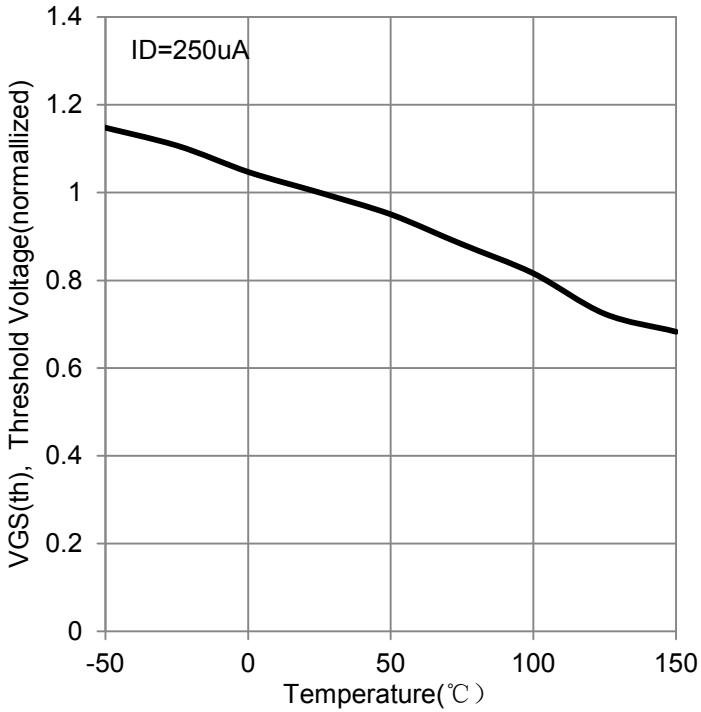


IS vs. VSD

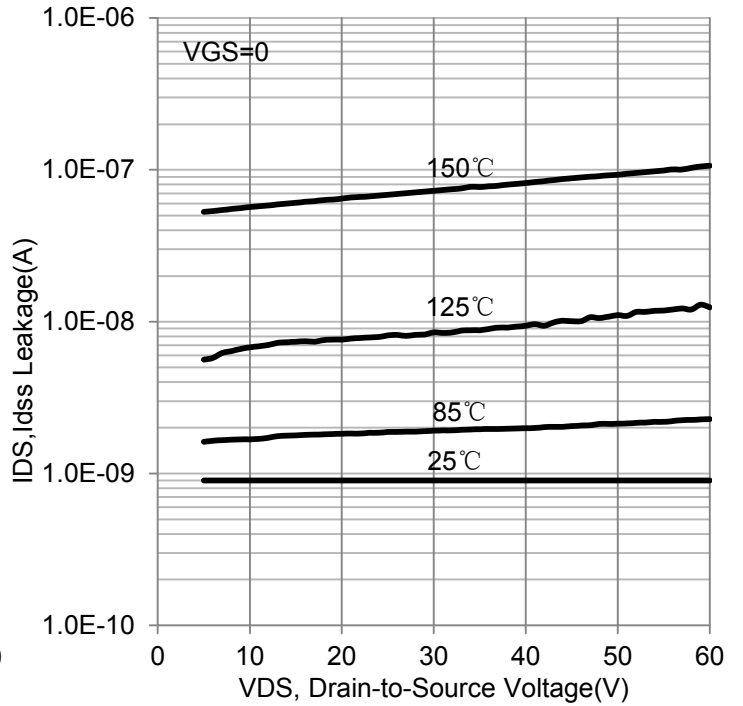
# DEVICE CHARACTERISTICS

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## 6. ELECTRICAL CHARACTERISTICS CURVES (Con.)



VGS(th) vs. Temperature



IDS vs. VDS

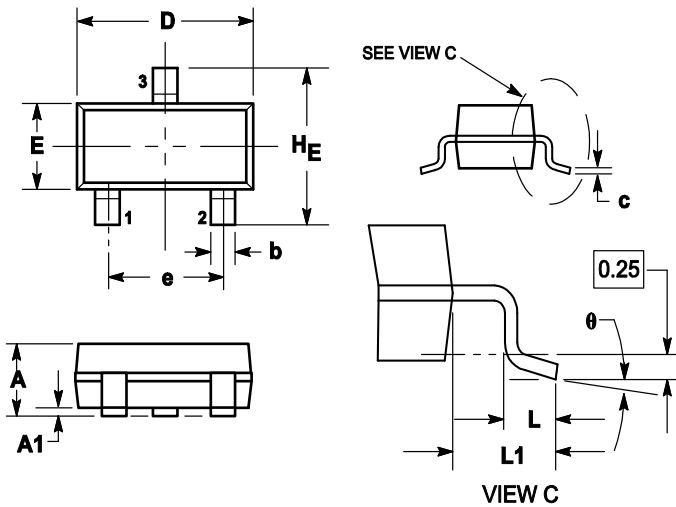
# PACKAGE OUTLINE & DIMENSIONS

## 2N7002

### 7. OUTLINE AND DIMENSIONS

Notes:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.



DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.89	1	1.11	0.035	0.04	0.044
A1	0.01	0.06	0.1	0.001	0.002	0.004
b	0.37	0.44	0.5	0.015	0.018	0.02
c	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.9	3.04	0.11	0.114	0.12
E	1.20	1.3	1.4	0.047	0.051	0.055
e	1.78	1.9	2.04	0.07	0.075	0.081
L	0.10	0.2	0.3	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.4	2.64	0.083	0.094	0.104
θ	0°	---	10°	0°	---	10°

### 8. SOLDERING FOOTPRINT

