



YEA SHIN TECHNOLOGY CO., LTD

2N7002SW

N-Channel Enhancement MOSFET



VDS= 60V, ID= 320mA

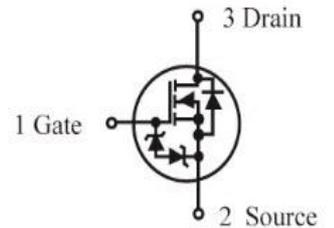
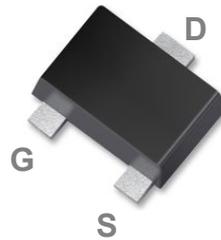
Features

- ESD portected
- Low RDS(on)
- Marking : 701

Application

- Low side load switch
- Level shift circuits
- DC-DC converter
- Portable applications i.e. DSC, PDA, Cell Phone, etc.

SOT-323 Pin Configuration



Ordering Information

Device	Qty per Reel	Reel Size
2N7002SW	3000	7Inch

Absolute Maximum Rating $T_a=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Steady State ($T_a=25^{\circ}\text{C}$)	320	mA
	Drain Current – Steady State ($T_a=85^{\circ}\text{C}$)	230	
	Drain Current – $t<5\text{s}$ ($T_a=25^{\circ}\text{C}$)	380	
	Drain Current – $t<5\text{s}$ ($T_a=85^{\circ}\text{C}$)	270	
I _{DM}	Pulsed Drain Current ($t_p=10\mu\text{s}$)	1.5	A
I _S	Source Current (Body Diode)	300	mA
P _D (Note1)	Total Device Dissipation – Steady State	300	mW
	Total Device Dissipation – $t<5\text{s}$	420	
R _{θJA} (Note1)	Junction to Ambient – Steady State	417	W/°C
	Junction to Ambient – $t<5\text{s}$	300	
T _L	Lead Temperature for Soldering Purposes (1/8" from case for 10s)	260	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
ESD	Gate-Source ESD Rating (HBM, Method 3015)	±2000	V

DEVICE CHARACTERISTICS

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Electrical Characteristics ($T_a=25^\circ\text{C}$, unless otherwise)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=60V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=60V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	500	
		$V_{DS}=50V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	100	nA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 10	μA

On Characteristics (Note 2)

$R_{DS(ON)}$	Static Drain-source On-Resistance	$V_{GS}=10V, I_D=500mA$	---	---	2.8	Ω
		$V_{GS}=4.5V, I_D=200mA$	---	---	3.2	Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1	---	2	V
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=200mA$	80	---	---	mS

Dynamic and Switching Characteristics

Q_g	Total Gate Charge	$V_{DS}=10V, V_{GS}=4.5V, I_D=500mA$	---	0.44	---	nC
Q_{gs}	Gate-Source Charge		---	0.2	---	
Q_{gd}	Gate-Drain Charge		---	0.1	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=30V, V_{GS}=10V, R_G=25\Omega, I_D=500mA, R_L=60\Omega$	---	2.7	---	ns
T_r	Rise Time		---	2.5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	13	---	
T_f	Fall Time		---	8	---	
C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, f=1MHz$	---	---	35	pF
C_{oss}	Output Capacitance		---	---	10	
C_{rss}	Reverse Transfer Capacitance		---	---	5	

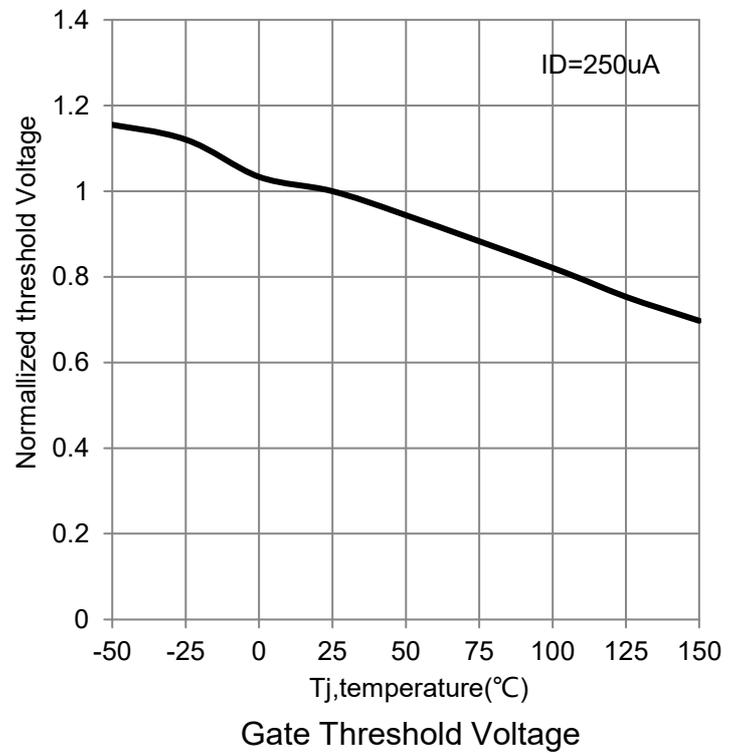
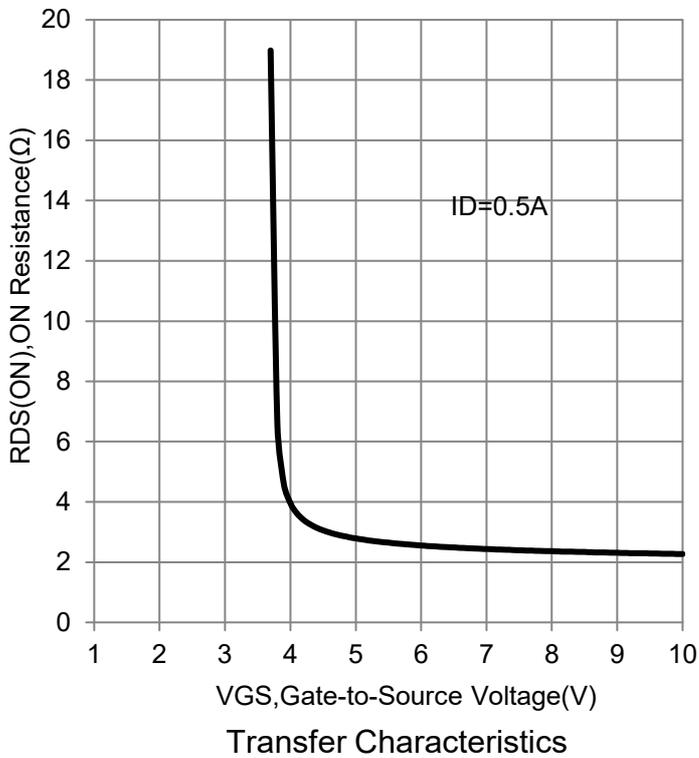
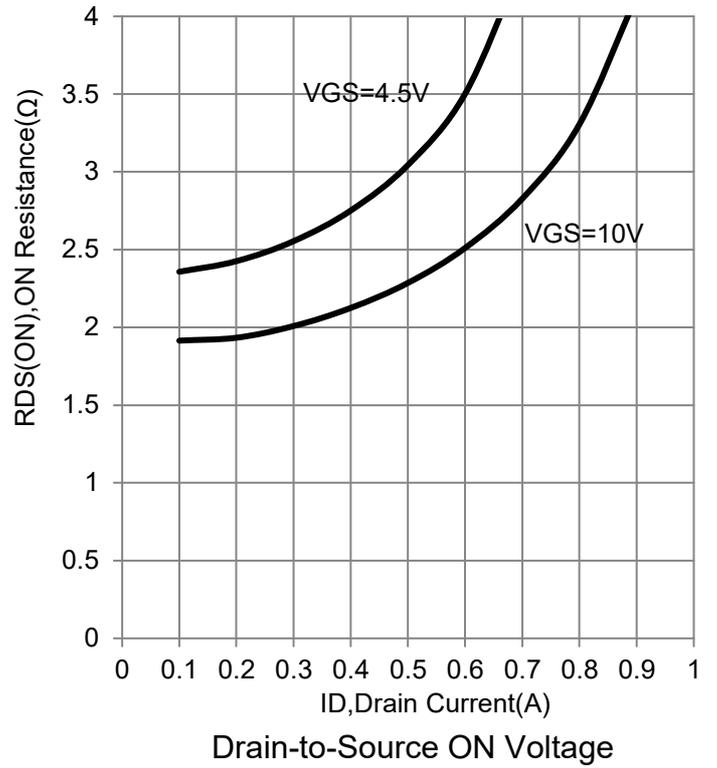
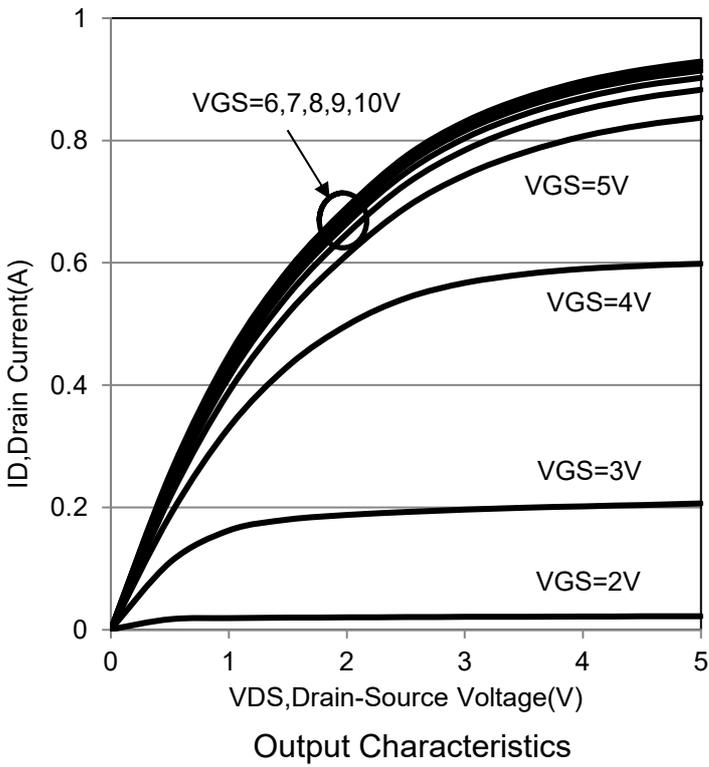
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=0.5A$	---	0.85	---	V

- FR-4 = 1*0.75*0.062 inch.
- Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

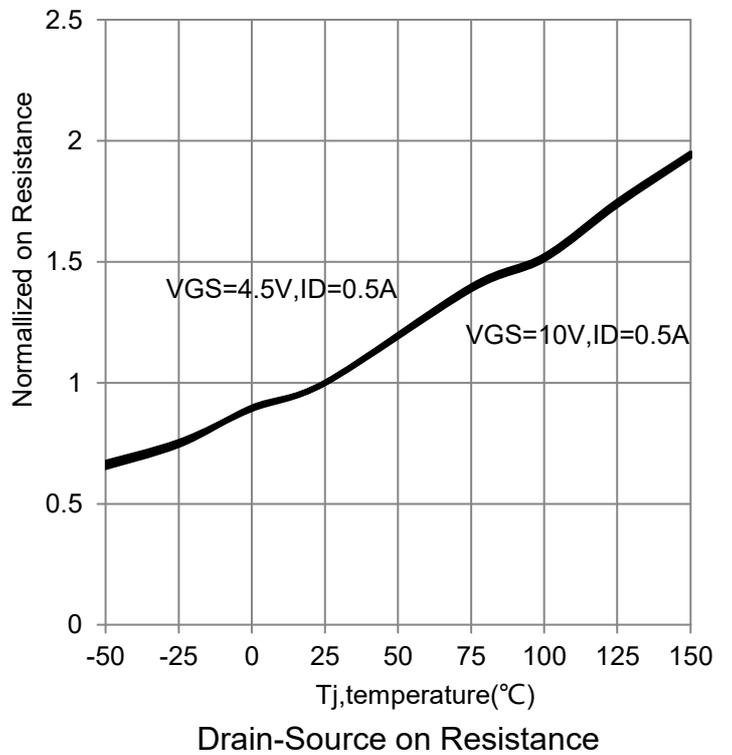
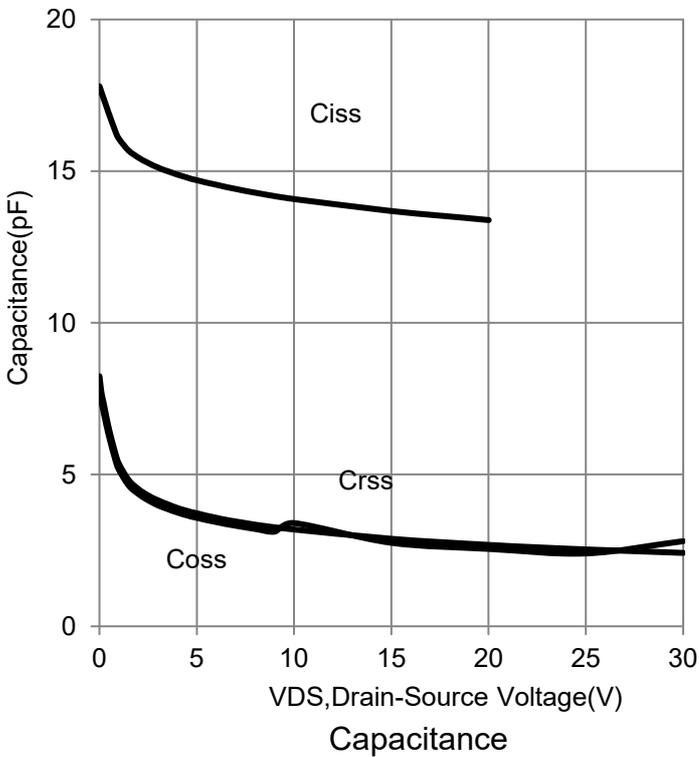
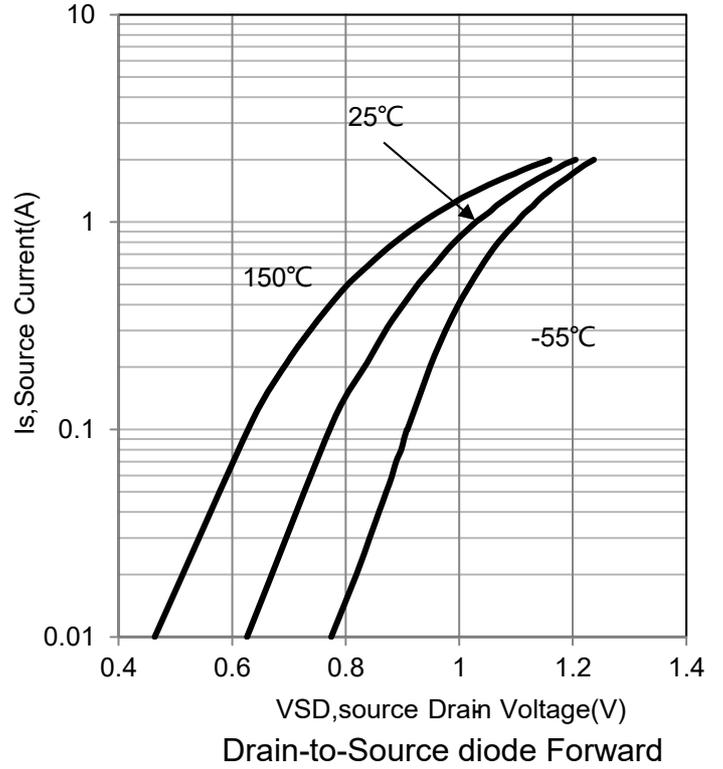
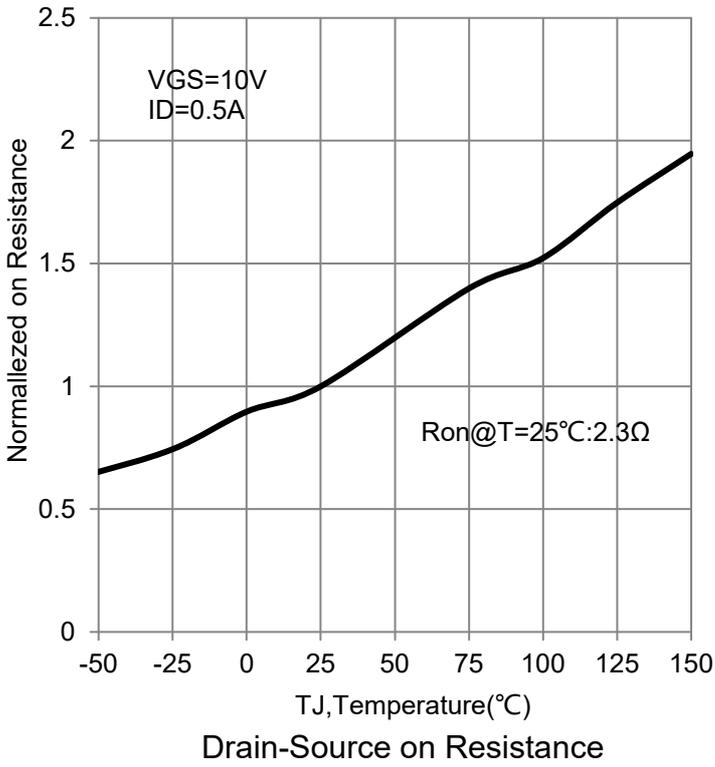
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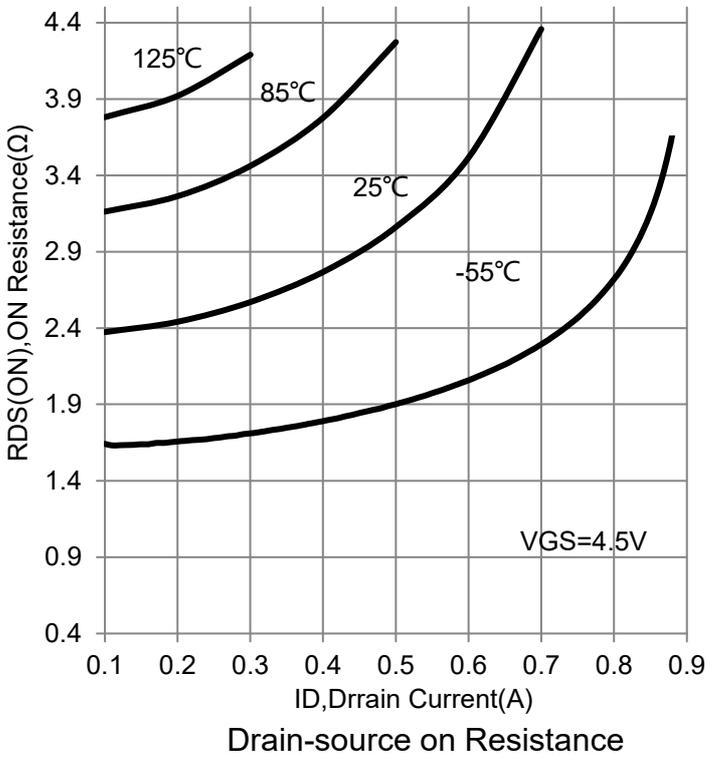
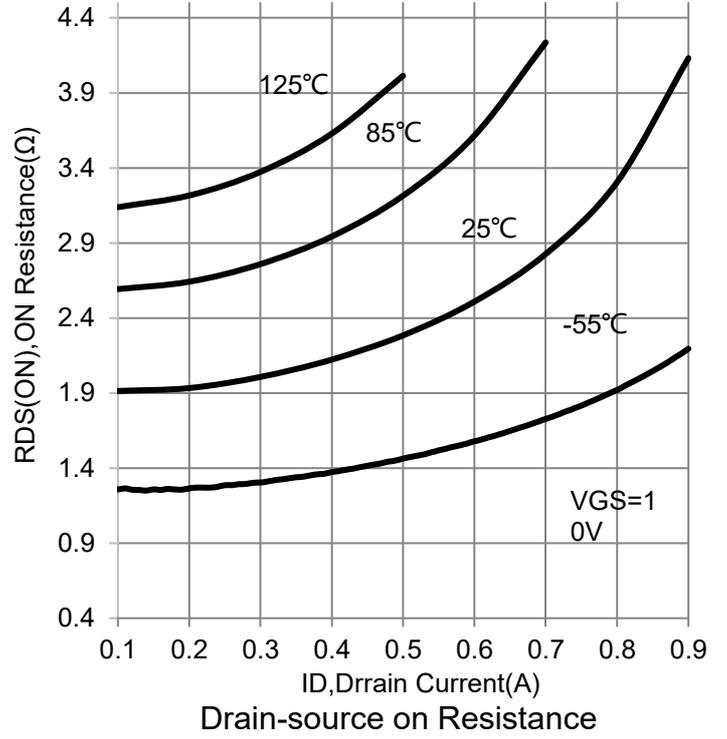
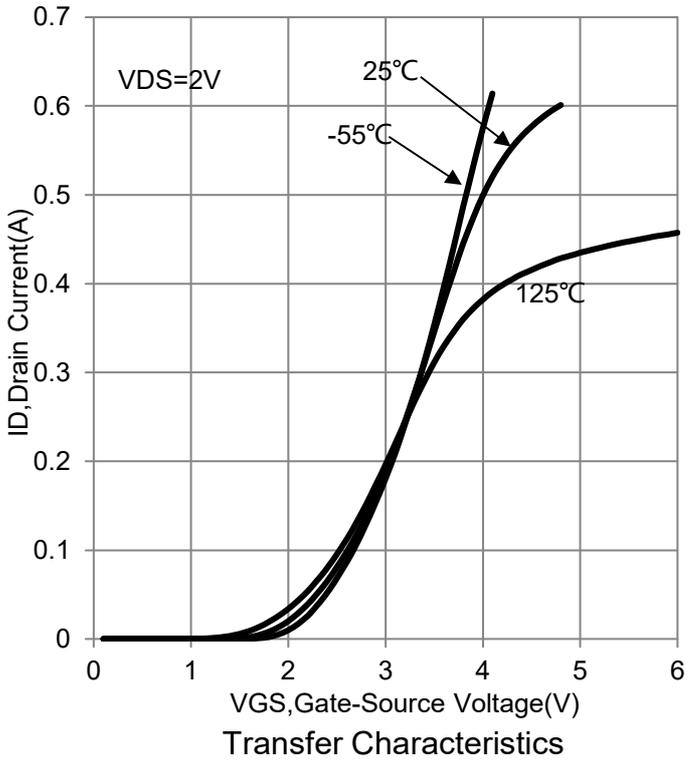
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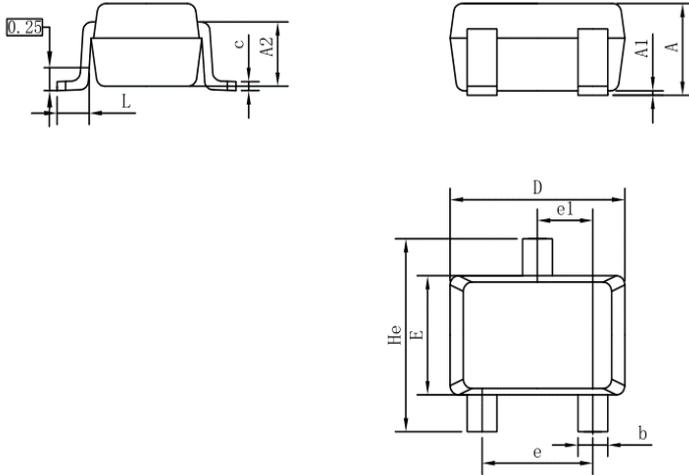
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PACKAGE OUTLINE & DIMENSIONS

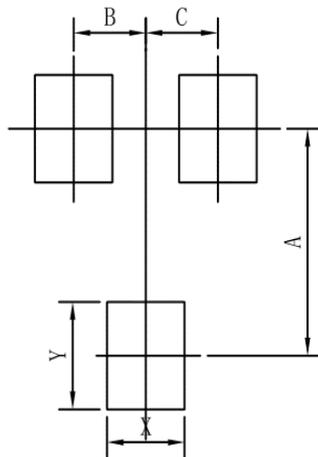
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Outline and dimensions



SOT-323			
DIM	MIN	NOR	MAX
A	0.80	0.95	1.00
A1	0.00	0.05	0.10
A2	0.7 REF		
b	0.30	0.35	0.40
c	0.10	0.15	0.25
D	1.80	2.05	2.20
E	1.15	1.30	1.35
e	1.20	1.30	1.40
e1	0.65 BSC		
L	0.20	0.35	0.56
He	2.00	2.10	2.40
ALL Dimension in mm			

Soldering footprint



SOT-323	
DIM	MIN
A	1.90
B	0.65
C	0.65
X	0.70
Y	0.90